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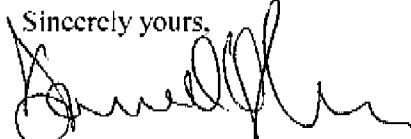
Examiner Chahandras Patel
Group Art Unit 2616
USPTO
Alexandria, Virginia
USA

Re: US Patent Application 10/699,254
FPGA BOOT-UP OVER A NETWORK
Our ref. 49452

Dear Mr. Patel:

Recognizing that Internet communications are not secure, I hereby authorize the USPTO to communicate with me concerning any subject matter of this application by electronic mail. I understand that a copy of these communications will be made of record in the application file.

I will call you and your SPE today at 2:30 PM EST at 571-270-1211. Please confirm by return e-mail.

Sincerely yours,

Daniel Kligler, Ph.D.
Reg. No. 41,120
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